

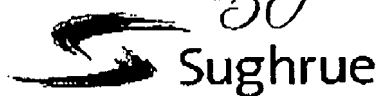
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Date October 19, 2007

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Subject Proposed Interview Agenda

Sughrue Ref Q78750 Application No. 10/802,010

Pages 2 (including cover sheet)

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APPLICATION

USSN: 10/802,010

Attorney Docket No. Q78750

Proposed Interview Agenda

Applicant's representative wishes to discuss the rejection of claims 2 and 12 under 35 U.S.C. 112, first paragraph.

Additionally, Applicant's representative would like to discuss the rejection of claims 1, 3-4, 11 and 13-14 under 35 U.S.C. 102(b). In particular, Applicant's representative would like to discuss the claimed feature, "the flash memory controller is configured to perform a write operation for writing the data and the meta-information allocated to the logical block in a new physical block without changing flash memory state information written in a previous physical block corresponding to the logical block if the previous write operation has been performed for the logical block[.]"